

## VISION

Empowering through Quality Technical Education.

## MISSION

Committed to serve the needs of the society at large by imparting state-of-the-art Engineering education and to provide **Knowledge** and develop **Attitudes, Skills** and **Values**, leading to establishment of quality conscious and sustainable research oriented **Education Institute**.

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## 1. ABOUT THE INSTITUTE

K. K. Wagh Institute of Engineering Education & Research (Formerly K. K. Wagh College of Engineering) was established in the year 1984 at Bhausaheb Nagar and shifted to Nashik City in September 1986. Shri Kakusheth Udesi of Hirabai Hansraj Charitable Trust, Mumbai generously donated a land of 8.2 hectares. The Education Society started building infrastructure at this campus (known as Hirabai Haridas Vidyanagari) in the year 1987. As on date it is fully developed and provides accommodation for office, classrooms, drawing halls, laboratories, workshop etc. Building, with built-up area of 42,575 Sq.m. is already completed and is being used. All Classrooms and Laboratories are designed as per the needs of the students. The Institute library has a good collection of reference and textbooks, periodicals and journals.

The Institute is approved by All India Council for Technical Education (AICTE), New Delhi and Government of Maharashtra. It is affiliated to University of Pune. The Government of Maharashtra has adjudged the Institute as Grade 'A'. NBA-AICTE, New Delhi has accredited Six Courses. The Teaching and Non-teaching Staff of the Institute is a blend of senior experienced and young dynamic faculty members devoted to the noble cause of education. Many of our students were the toppers in the University examination and are in great demand from industries in India and abroad. Our Training and Placement wing tries hard to seek good jobs for our students. In addition to the academics, the students are engaged in sports, co-curricular and extra-curricular activities to provide a healthy relief from rigorous routine. The Institute is having a spacious play ground and the modern facilities for both indoor and out door games with well equipped Gymnasium for bodybuilding and fitness. Due to able and proper guidance and motivation, many of our students have represented the University at National and International level in different sport events.

The Institute at present provides four-year course leading to Bachelor's Degree of University of Pune in the following branches of Engineering.

Name of The Branch	Year of Establishment	Sanctioned Intake
▪ Computer Engineering	1984	120
▪ Electronics & Telecommunication	1984	60
▪ Production Engineering	1984	60
▪ Civil Engineering	1985	60
▪ Mechanical Engineering	1995	120
▪ Electrical Engineering	1996	60
▪ Chemical Engineering	1999	60
▪ Information Technology	2001	60
<b>Total</b>		<b>600</b>

The Institute is recently permitted to start Mechanical Engineering and Electronics Engineering degree course in the Second Shift.

The Institute also provides following PG Courses of two year duration leading to Master's Degree in Engineering of University of Pune in following Branches and Research Centre in Production, Civil & Electrical Engineering

Name of The Branch	Year of Establishment	Sanctioned Intake
▪ Production Engineering (Manufacturing & Automation)	2006	18
▪ Civil Engineering (Structure Engineering)	2007	18
▪ Electrical Engineering (Control System)	2007	18
▪ M.C.A.	2007	60
▪ M.B.A.	2008	60
▪ Computer Engineering	2010	18
▪ Electronics Engineering (VLSI & Embedded Systems)	2010	18
<b>TOTAL</b>		<b>210</b>

## 2. PG Courses

### 2.1) M.E. Computer Engineering

Department of Computer Engineering was established in the year 1984 and accredited by NBA, AICTE providing quality technical education in the area of Computer Engineering in order to meet challenges of globalization. The department along with institution emphasizes all round development of students. A department has a well-qualified and experienced faculty. The faculty members play dual role both as teachers to impart technical knowledge to students as well as counselors to guide them. It is said that while learning it is good to hear, better to read and best to observe. Visualization of what one reads, leads to the reinforcement of the knowledge. Laboratories go a long way in this direction. With access to such laboratories, the students get hands on experience with latest technology, research and testing methods. The department has maintained a good liaison with the industries. Various experts from industry are invited to deliver special lectures for the students. Visits are arranged to various industries for the students to bridge the gap between theory and practice.

Apart from the academic excellence, the department is striving to impart quality education and carry out research in the field of Computer Engineering. The objective is to achieve most favored status from the industry. The main focus of the department is on increasing the interaction with industries and updates the skills in today's competitive scenario.

M.E. Computer Engprogramme students are exposed to latest technology and take up dissertation work related to industrial problems. As Nashik is a fastest growing Industrial city, it has tremendous potential for the job opportunities for Post Graduate students. With rising employment especially in the IT industries, recruitment agencies, training institutions, HR outsourcing, Legal services, IT infrastructure management, and other business-to-business outfits are also keeping pace with the developments. This institute, running the course of Computer Engineering for the last 25 years feels that it has necessary infrastructure and trained manpower to provide education to aspirant IT professionals and thus can play a significant role in providing required manpower to satisfy the increasing demand of the IT and other industries.

#### Available Softwares:

<ul style="list-style-type: none"><li>▪ <b>Compilers</b><ul style="list-style-type: none"><li>• Turbo C</li><li>• C++</li><li>• COBOL</li><li>• Visual Studio</li></ul></li></ul>	<ul style="list-style-type: none"><li>▪ <b>Application Softwares</b><ul style="list-style-type: none"><li>• IBM Rational</li><li>• Oracle 8i</li><li>• 3d Studio Max</li><li>• MATLAB</li><li>• QTP</li></ul></li></ul>
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#### List of Laboratories

Sr. No.	Name of Laboratory
1.	Multimedia Lab
2.	Network Lab
3.	Unix Lab - I
4.	Unix Lab - II
5.	Project Lab
6.	PG Lab
7.	Software Lab - I
8.	Software Lab – II
9.	Software Lab – III
10	Research Lab
11	Microprocessor Lab
12	Basic Electronics Lab

## List of Faculty Members in the Department

Sr. No.	Name of Staff	Designation	Highest Qualification	Teaching Exp (Yrs)
1.	Mr. Sane S. S.	Professor & Head	PhD	22
2.	Mr. Shahane N. M.	Assistant Professor	ME (Electronics Specialization: Computer)	19
3.	Mr. Jhade M. B.	Assistant Professor	MTech(Electronics)	18
4.	Mrs. Khedkar A. P.	Assistant Professor	ME(Electronics)	18
5.	Mrs. Kamlapur S. M.	Assistant Professor	ME(Computer)	13
6.	Mrs. Deshpande N. A.	Assistant Professor	ME(Computer)	13
7.	Mrs. Kulkarni R. D.	Assistant Professor	ME(Computer)	8
8.	Mrs. Chaudhari J. C.	Lecturer	ME(Computer)	4
9.	Mr. Banait S. S.	Lecturer	BE(Computer), ME (Comp) Appeared	7
10.	Ms. Kulkarni A. A.	Lecturer	BE(Electronics)	8
11.	Ms. Pawar Y. P.	Lecturer	BE(Computer)	5
12.	Ms. Kulkarni P. A.	Lecturer	BE(Computer)	5
13.	Ms. Bapat S. G.	Lecturer	BE(Computer)	5
14.	Ms. Pawar S. B.	Lecturer	BE(Computer)	3
15.	Mrs. Ambekar N. S.	Lecturer	BE(Computer)	2
16.	Ms. Jadhav R. H.	Lecturer	BE(Computer)	2
17.	Ms. Sharma N. G.	Lecturer	BE(Computer)	4
18.	Ms. C. S. Priyadarshini	Lecturer	BE(Computer)	2
19.	Mr. Bachhav N. Y.	Lecturer	BE(Computer)	1
20.	Ms. Metkar M. J.	Lecturer	BE(Computer)	1
21.	Ms. Deshmukh C. S.	Lecturer	BE(Computer)	1
22.	Mr. Kolapkar A. V.	Lecturer	BE(Computer)	2
23.	Mr. Kanade D. M.	Lecturer	BE(Computer)	1
24.	Mr. Pingle W. W.	Lecturer	BE(Computer)	2
25.	Ms. Indulkar R. V.	Lecturer	BE(Computer)	2
26.	Ms. Ghodke M. S.	Lecturer	BE(Computer)	1
27.	Ms. Gunjal C. S.	Lecturer	BE(Computer)	2
28.	Mr. Rao P. D.	Lecturer	BE(Computer)	-
29.	Ms. Nikam P. D.	Lecturer	BE(Computer)	-

## List of Visiting Faculties

Following teachers will offer their services in teaching of PG classes

- Dr. S. A. Kelkar, IIT Powai
- Dr. R. S. Tiwari, Director, School of Computer Science, YCMOU, Nashik
- Dr. M. U. Kharat, Nashik
- Dr. Parvati Rajan, Nashik
- Dr. Uday P. Wad, Nashik
- Mr. Rohit Kulkarni, Newman Computers, Nashik

## COURSE STRUCTURE FOR M.E. COMPUTER ENGINEERING

### Semester- I

Subject Code	Subject Title	Teaching Scheme		Examination Scheme				
		Lect.	Pract.	Paper	TW	Oral	Total	Credits
510101	Applied Algorithms	03	--	100	--	--	100	3
510102	Emerging Trends in Computer Architecture	03	--	100	--	--	100	3
510103	Principles and Practices for IT Management	03	--	100	--	--	100	3
510104	Elective-I	03	--	100	--	--	100	3
510105	Elective-II	03	--	100	--	--	100	3
510106	Laboratory Practice-I	--	06	--	50	--	50	3
510107	Seminar-I	--	04	--	50	--	50	2
	<b>TOTAL</b>	<b>15</b>	<b>10</b>	<b>500</b>	<b>100</b>	<b>--</b>	<b>600</b>	<b>20</b>

### Semester- II

Subject Code	Subject Title	Teaching Scheme		Examination Scheme				
		Lect.	Pract.	Paper	TW	Oral	Marks	Credits
510108	Distributed System	03	--	100	--	--	100	3
510109	High Performance Database Systems	03	--	100	--	--	100	3
510110	Network Design, Modeling and Analysis	03	--	100	--	--	100	3
510111	Elective-III	03	--	100	--	--	100	3
510112	Elective-IV	03	--	100	--	--	100	3
510113	Laboratory Practice-II	--	06	--	50	--	50	3
510114	Seminar-II	--	04	--	50	--	50	2
	<b>TOTAL</b>	<b>15</b>	<b>10</b>	<b>500</b>	<b>100</b>	<b>--</b>	<b>600</b>	<b>20</b>

Semester- III

Subject Code	Subject Title	Teaching Scheme		Examination Scheme				
		Lect.	Pract.	Paper	TW	Oral	Total	Credits
510115	Seminar-III	--	04	--	50	--	50	2
510116	Project Stage-I	--	18	--	50	--	50	6
	<b>TOTAL</b>	--	<b>22</b>	--	<b>100</b>	--	<b>100</b>	<b>8</b>

Semester- IV

Subject Code	Subject Title	Teaching Scheme		Examination Scheme				
		Lect.	Pract.	Paper	TW	Oral	Marks	Credits
510116	Project Stage-II*	--	18	--	150	50	200	12
	<b>TOTAL</b>	--	<b>18</b>	--	<b>150</b>	<b>50</b>	<b>200</b>	<b>12</b>

\* The term work of project stage II of semester IV should be assessed jointly by the pair of internal and external examiner along with the oral examination of the same

Subject Code	Elective-I	Subject Code	Elective-I
510104 A	Advance Software Engineering	510105 A	Information and Network Security
510104 B	Intelligent Systems	510105 B	Advanced Compilers
510104 C	Internet Routing Design	510105 C	Web Services and SOA
510104 D	Mobile Computing	510105 D	Embedded System Design

Subject Code	Elective-III	Subject Code	Elective-IV
510111 A	Software Design and Architecture	510112 A	Software Project Management
510111 B	Pattern Recognition and Machine Vision	510112 B	Infrastructure Management
510111 C	Network Programming	510112 C	Data Warehousing and Data Mining
510111 D	Advanced Internet Programming	510112 D	Open Elective (Self Study)**

\*\* Open elective subject –BOS computer engineering will declare the list of subjects which can be taken under open elective.

## 2.2) Electronics and Telecommunication Engineering Department (VLSI & Embedded Systems)

An Electronics and Telecommunication Engineer learns more about communication systems, Networking and Internet, Cellular Communication Systems, Semiconductor devices for low power applications as well as for high power applications. It is a combination of Analog and Digital Electronics Systems. The different areas are Analog and Digital Communication, Cellular Communication, Microwave and Radar Systems, Digital Systems, Microprocessors, Microcontrollers, VLSI Design, Embedded System, etc.

At K. K. Wagh, the Electronics and Telecommunication Engineering Department is a group of self-motivated engineers. Apart from academic excellence, the Department is also striving to impart quality education and carry out research in the related field of electronics and telecommunication engineering. The objective is to achieve most favoured status from the Telecom and IT industry. The main focus of the department is on increasing the interaction with industry, various government organizations, Institutes like IITs, NITs, and NITTTR and update the skills of the faculty members so that they can impart the world class knowledge required in today's competitive scenario.

Department has well-equipped Laboratories; with access to such laboratories, the students get a hands on experience with latest technology, research and testing methods. The department encourages the staff members to present research papers at National and International conferences. Many UG students of the department participate in National Level Paper Presentation Contests and also have won the prizes. Department organizes National level paper presentation conference once in three years for UG students.

The students passed out in the last two decades from the Electronics Engineering Department have been successfully placed in various private sector and public sector organizations. Many students have joined prestigious universities from India and abroad for higher studies and after completing the course, they are working successfully with Multinational companies. Some of our students are successful entrepreneurs. The department has Alumni Association for continuous interaction with the past students.

The department has maintained a good liaison with the Institutes and Industry in and around Nashik. Various experts from industry are invited to deliver special lectures for the students. Visits are arranged to various Industries and Technical Exhibitions for the students to bridge the gap between theory and practice. Many under graduate students undergo vocational training in Industries like HAL, Siemens, etc. This training in industry helps our students to get exposure to industry working environment.

### Goals of the Electronics and Telecommunication Department

The goal of the department is to establish a center of excellence in emerging areas of electronics and telecommunication engineering, to increase interaction with the industry, to encourage students to become entrepreneurs and to facilitate the all round development of students. The department has excellent state-of-the-art laboratory setup, audio-visual aids and self-motivated staff to achieve the excellence in these areas of engineering. The department student's association arranges seminars, workshops, slide shows and field visits for all round development of the students. Department also promotes extracurricular activities for over all development of students. To provide better placement opportunities for the students institute has a state of art training and placement department.

### Major Equipments available in the department

- ❖ Logic Analyser
- ❖ Spectrum Analyser
- ❖ DSO
- ❖ Embedded System Trainers
- ❖ VLSI Trainers/Programmer

### Major Softwares available in the department:

- ❖ Xilinx Foundation Series
- ❖ MATLAB 7.1
- ❖ RTOS
- ❖ ORCAD

**STAFF LIST**  
**Electronics & Telecommunication Department**

Sr. No.	Name	Designation	Qualification	Total Experience (Yrs)
1.	Dinesh M. Chandwadkar	Professor & Head	M.E. (Electronics)	21
2.	Vitthal J. Gond	Assistant Professor	M.TECH. (EDT)	21
3.	Kailas S. Holkar	Assistant Professor	M.E. (Instrumentation)	19
4.	Sunil S. Morade	Assistant Professor	M.TECH. (Electronics)	16
5.	Manisha P. Satone	Assistant Professor	M.E. (Electronics)	20
6.	Sanjivani S. Bhabad	Assistant Professor	M.E. (Electronics)	17
7.	Sunita P. Ugale	Assistant Professor	M.TECH. (EDT)	14
8.	Ramesh R. Khinde	Lecturer	B.E. (Electronics)	5
9.	Mayuri U. Joshi	Lecturer	B.E. (Electronics)	4
10.	Deepali Wadkar (Shimpi)	Lecturer	B.E. (Electronics & Telecommunication)	4
11.	Vaishali R. Lele	Lecturer	B.E. (Electronics)	4
12.	Rupali Nerkar (Chothe)	Lecturer	B.E. (Electronics & Communication)	3
13.	Vishwas R. Takate	Lecturer	B.E. (Electronics)	1
14.	Smita C. Shinde	Lecturer	B.E. (Electronics)	1

**List of Visiting Faculties**

Following teachers will offer their services in teaching of PG classes

- Dr. Ashish Bhargave(Ph.D.), Nashik
- Mr. Ravindra Wadekar (M.Tech.), Nashik
- Mr. Pravin Ambekar (M.Tech.), Nashik

**COURSE STRUCTURE FOR M.E. (E & TC/ELECTRONICS)**  
**(VLSI and Embedded Systems)**

(For 2008 course - w.e.f. June 2008)

**SEMESTER I**

CODE	SUBJECT	TEACHING SCHEME		EXAMINATION SCHEME					CREDITS
		Lect.	Pr.	Paper	TW	Oral	Pr	Total	
504181	Analog and Digital CMOS IC Design	3	-	100	-	-	-	100	3
504182	Principles and Practices for IT Management	3	-	100	-	-	-	100	3
504183	Embedded System Design	3	-	100	-	-	-	100	3
504184	Elective I	3	-	100	-	-	-	100	3
504185	Elective II	3	-	100	-	-	-	100	3
504186	VLSI Embedded Practice I	-	6	-	50	-	-	50	3
504187	Seminar I	-	4	-	50	-	-	50	2
<b>Total of Second Term</b>		<b>15</b>	<b>10</b>	<b>500</b>	<b>100</b>	<b>-</b>	<b>-</b>	<b>600</b>	<b>20</b>

### SEMESTER II

CODE	SUBJECT	TEACHING SCHEME		EXAMINATION SCHEME					CREDITS
		Lect.	Pr.	Paper	TW	Oral	Pr	Total	
504188	Real Time Operating Systems	3	-	100	-	-	-	100	3
504189	Embedded Signal Processing	3	-	100	-	-	-	100	3
504190	RF IC Design	3	-	100	-	-	-	100	3
504191	Elective III	3	-	100	-	-	-	100	3
504192	Elective IV (Open)	3	-	100	-	-	-	100	3
504193	VLSI Embedded Practice II	-	6	-	50	-	-	50	3
504194	Seminar II	-	4	-	50	-	-	50	2
<b>Total of Second Term</b>		<b>15</b>	<b>10</b>	<b>500</b>	<b>100</b>	-	-	<b>600</b>	<b>20</b>

### SEMESTER III

CODE	SUBJECT	TEACHING SCHEME		EXAMINATION SCHEME					CREDITS
		Lect.	Pr.	Paper	TW	Oral	Pr	Total	
604181	Seminar III	-	4	-	50	-	-	50	2
604182	Project Stage I	-	18	-	50	-	-	50	6
<b>Total of Third Term</b>		-	<b>22</b>	-	<b>100</b>	-	-	<b>100</b>	<b>8</b>

### SEMESTER IV

CODE	SUBJECT	TEACHING SCHEME		EXAMINATION SCHEME					CREDITS
		Lect.	Pr.	Project	TW	Oral	Pr	Total	
604183	Project Stage II	-	18	150*	-	50	-	200	12
<b>Total of Fourth Term</b>		-	<b>18</b>	<b>150</b>	-	<b>50</b>	-	<b>200</b>	<b>12</b>

\* The Term Work of Project stage II of semester IV should be assessed jointly by the pair of internal and external examiners along with the oral examination of the same.

Note: The Contact Hours for the calculation of load of teacher Seminar – 1Hr / week / student & Project – 2Hr / week / student.

#### Elective I:

1. ASIC Design and Modeling
2. Nanotechnology
3. Machine Intelligence

#### Elective III:

1. Reconfigurable Computing
2. Memory Technologies
3. VLSI EDA Tools

#### Elective III:

1. Fault Tolerant System Design
2. Biomedical Signals and Systems
3. Advanced Digital System Design

#### Elective IV (OPEN):

1. Embedded Automotive Systems
2. System-on-Chip (SoC)
3. Software Defined Radio

#### Or any one subject of Elective IV from the following branches

1. Computer Engineering
2. Information Technology

### 3. ADMISSION & ELIGIBILITY CRITERIA

#### 3.1

##### A) M E Computer Engineering

- B.E. /B.Tech. in Computer Engineering / Information Technology and equivalent **OR** Passing A.M.I.E. /A.M.I.E.T.E. after 3 year diploma with valid GATE score in respective branches.
- Preference will be given to candidates possessing valid GATE score in relevant branch.

##### B) M.E. Electronics and Telecommunication Engineering Department (VLSI & Embedded Systems)

- B.E. / B.Tech. in Electronics, Electronics & Telecommunication, Instrumentation & Control, Industrial Electronics, Computer Engineering and equivalent **OR** Passing A.M.I.E./A.M.I.E.T.E. after 3 year diploma with valid GATE score in respective branches.
- Preference will be given to candidates possessing valid GATE score in relevant branch

For detail refer [http://www.unipune.ac.in/pdf\\_files/58\\_Eligibility.pdf](http://www.unipune.ac.in/pdf_files/58_Eligibility.pdf)

#### **NOTE:**

- 1) Diploma holders who have passed section A & B examinations of Institution of Engineers (India), Kolkata and possessing valid GATE score are also considered eligible for admission to M.E. Degree courses in the respective branches.
- 2) Admission will be effected according to the merit list prepared on the basis of valid GATE Score in the respective branch only. GATE Score of one branch will not be valid for admission in another branch.
- 3) In case, GATE qualified candidates from General Category & SC/ST are not available, the admission will be offered based on the merit list prepared after Institute test / interview to NON-GATE candidates who have applied and qualified.
- 4) Candidate claiming to belong to reserved category will be required to produce a caste certificate, caste validity certificate, Income Certificate, Non Creamy Layer certificate (if applicable) to that effect from appropriate authorities.
- 5) The admission will be given based on common merit list prepared.
- 6) Scholarship will be provided to non-sponsored GATE candidate only according to rules of AICTE, as per availability of funds from AICTE, New Delhi. The scholarship will continue to be received provided candidate fulfills the conditions prescribed by AICTE, from time to time.

#### 3.2 SPONSORED CANDIDATES

##### Eligibility

- Candidates who possess Bachelor's degree in the respective or allied branch of Engineering/Technology will be eligible for admission to the course.
- In addition to the above, the candidate will have to fulfill the eligibility requirements prescribed by University of Pune, if any.
- The candidate must have minimum of 2 years of full time work experience in a registered firm / company / industry / educational and / or research institute / any Government Department or Government Autonomous Organization in the relevant field.
- A letter from the employer must be furnished stating that the candidate is being sponsored to seek admission to the postgraduate degree program (Refer Appendix 'A'). The employer should also indicate that the candidate will not be withdrawn midway till the completion of the course. The candidate will be fully under the administrative control of the institute for the period of the course.
- Only those employed candidates who, at the time of admission, are permitted by their respective organizations referred above will be eligible for claiming seats under sponsored category.
- Sponsored candidates will be required to appear for Institute Test/Interview along with the NON-GATE candidate and a common merit list will be prepared.

### 3.3 ADMISSION PROCEDURE

- Candidates will be considered for the admission on merit, based on total marks secured, out of 100 marks as indicated below  
Entrance Test            70 marks  
Interview                 30 marks
- Candidate securing less than 40 marks out of 100 shall not be eligible for admission.
- If the total performance of the candidates is comparable, then the preference shall be given for the admission to the candidate who are GATE qualified and those who are serving in Engineering College, Polytechnic and Industry, in that order.

#### Stipend / Scholarship

- Sponsored candidates will not be eligible for any Stipend / Scholarship. All the rules and regulation regarding stipend/scholarship to GATE candidates will be as per AICTE directives received from time to time

#### Reserved Seats

- No reservation is provided in the sponsored category. However preference will be given to reserved category in case of equal merit.

#### Entrance Test

- Entrance test will be conducted as per the schedule prepared by the institute.
- The syllabus for the entrance test will be the UG syllabus of relevant branch as prescribed by University of Pune.
- The entrance test will be conducted at concerned department of K. K. Wagh Institute of Engineering Education & Research, Nashik.

### 3.4 NUMBER OF SEATS AVAILABLE (In each course)

Category	Open	SC	ST	Sponsored	Total
Number of Seats	10	02	01	05	18

### 4. APPLICATION PROCEDURE

Application forms & brochures are available on our institute web site [www.kkwagh.org](http://www.kkwagh.org) Candidates desiring to take admission for ME programmes, can download the application form, fill it & send to the Principal KK Wagh Institute of Engineering Education & Research, Panchavati, Nashik, along with a crossed Demand Draft of Rs. 300/- drawn in favor of '**Principal, K. K. Wagh Institute of Engineering Education & Research, Nashik**' payable at Nashik. Application & Brochure are also available at the Institute office on cash payment of Rs. 300/-.

#### Note

- a) After carefully reading all the instructions given in the brochure, fill in the application form.
- b) Form should be filled neatly and cleanly.
- c) Candidate should complete the application form in all respects and attach all necessary documents. Incomplete application forms will be rejected.
- d) SC / ST candidates are requested to attach Caste / Tribe certificate along with the Caste Validity Certificate.
- e) The candidates who have appeared for their final year of graduate examination in Engineering may also submit the application form and appear for the entrance test & interview. However he / she must produce the final year marksheet/provisional passing certificate obtained from University / Institute at the time of admission.

## 5. FEES AND DEPOSITS

The fee structure will be as per the guidelines and Fee Fixation by Shikshan Shulka Samiti. The details of fee structure will be made available in the Institute Office at the time of Written Test / Interview.

## 6. Admission Schedule

Activity	Date
Date of Notification on web Site <a href="http://www.kkwagh.org">www.kkwagh.org</a>	02/08/2010
Sale of Application Form begins	02/08/2010
Last date of submitting Forms (By post or by hand)	14/08/2010
Entrance Test (10.00 am) and Interview	16/08/2010
Display of List of selected Candidates	17/08/2010
Last date of Admission for selected candidates	28/08/2010
Commencement of Course	30/08/2010

### NOTE:

1. Candidates appeared for final year BE can also appear for Entrance Test and interviews.
2. Application forms are also available in Institute Office on payment of Rs.300/- by cash or by DD.
3. Candidates appearing for entrance test and interview should bring all necessary documents for verification.
4. Selected candidates failing to take admission by the due date 30/08/2010 will loose their claims for admission to P.G. programme at this Institute.

## 7. CANCELLATION OF ADMISSION, HOSTEL ACCOMMODATION & DISCIPLINE

### 7.1 Cancellation of Admission & Refund of Fees

- a) If the candidate desires to cancel his/her admission for any reason whatsoever during the admission process, he/she shall be allowed to do so and it shall be presumed that admission is cancelled at that stage.
- b) The candidate desirous of canceling admission shall apply for cancellation in the Proforma-1 attached in this booklet.
- c) Fees paid shall be refunded to the candidate after deducting cancellation charges of Rs.1000/- (Rs. One thousand only) if the seat is cancelled before commencement of the classes. If the seat is canceled after commencement of the classes, proportionate fees will be deducted. However if the vacant seat created due to cancellation is not filled, no refund shall be made.
- d) Leaving Certificate will be returned to candidate at the time of cancellation of admission.

### 7.2 Hostel Accommodation

Hostel accommodation will be provided to few Post Graduate Students on first come first served basis.

Details of Fees	For Boy's Hostel	For Girl's Hostel
Room Rent	13,000/-	14,000/-
Mess Charges	11,000/-	11,000/-
Caution Money (Refundable)	2,000/-	2,000/-
<b>Total</b>	<b>26,000/-</b>	<b>27,000/-</b>

### **7.3 Conduct and Discipline**

**7.3.1** Students while studying in Engineering Institute, if found indulging in anti-national activities contrary to the provisions of acts and laws enforced by the Government, any activity contrary to rules of discipline will be liable to be expelled from the college, without any notice, by the principal of the Institute.

**7.3.2** Candidates applying for admission against the seats reserved for backward classes mentioned in these Rules should note that only those candidates who actually belong to those castes and communities will be eligible for concession, as per prevailing Government directives.

**7.3.3** If any of the statements made in application form, any information supplied by the candidates in connection with his/her admission is later on, at any time, found to be false or incorrect, his/her admission will be cancelled, fees will be forfeited and he/she may be expelled from the college by the Principal and prosecuted, if deemed necessary. An appeal against the order of expulsion, however, may be referred to the Director of Technical Education, Maharashtra State, whose decision in such cases will be final.

**7.3.4** All candidates for admission to post graduate courses in Engineering/Technology are required to give the following undertaking in the attached format:

- (a) I have read all the Rules of Admission for the current year and after understanding these rules, I have filled in this form of application for admission for the current year.
- (b) I am aware of the law regarding prohibition of ragging as well as the punishments, and if I am found guilty of offence of ragging and / or abetting ragging, I am liable to be punished appropriately.
- (c) The information given by me in my application is true to the best of my knowledge and belief.
- (d) I have not been debarred from appearing at any examination held by any Government constituted or statutory examination authority in India.
- (e) I fully understand that the offer of a course or branch of engineering will be made to me depending on merit inter-se and availability of a seat at the time of scrutiny of my application, when I will report to the admission authority according to the schedule of admission.
- (f) I understand that no other document, other than those attached to the application form before the last date of admission will be entertained for the purpose of claims/concessions etc. in connection with my admission.
- (g) I hereby agree to confirm to any rules, acts and laws enforced by Government and I hereby undertake that so long as I am a student of the College, I will do nothing either inside or outside the college, which may result in disciplinary action against me under the rules, acts and laws.
- (h) I fully understand that the Principal of the college where I would be admitted, will have right to expel / rusticate me from the college for any infringement of the Rules of conduct and prescribed by the college/university (if any).



Karmaveer Kakasaheb Wagh Education Society's

**KARMAVEER KAKASAHEB WAGH INSTITUTE OF  
ENGINEERING EDUCATION & RESEARCH**

Hirabai Haridas Vidyanagari, Amrut Dham, Panchavati, NASHIK-422 003, (Maharashtra).

Ph. No.(0253) 2516671,2512876,Fax-2511962,2621183, 2518870

Mail: [kkwe\\_office@dataone.in](mailto:kkwe_office@dataone.in) / [kkwieer@gmail.com](mailto:kkwieer@gmail.com)

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**UNDERTAKING**

**(To be enclosed with Application Form)**

I hereby give an undertaking that:

1. I have read all the Rules of Admission for the current year and after understanding these rules, I have filled in this form of application for admission for the current year.
2. I am aware of the law regarding prohibition of ragging as well as the punishments, and if I am found guilty of offence of ragging and / or abetting ragging, I am liable to be punished appropriately.
3. The information given by me in my application is true to the best of my knowledge and belief.
4. I have not been debarred from appearing at any examination held by any Government constituted or statutory examination authority in India.
5. I fully understand that the offer of a course or branch of engineering will be made to me depending on merit inter-se and availability of a seat at the time of scrutiny of my application, when I will report to the admission authority according to the schedule of admission.
6. I understand that no other document, other than those attached to the application form before the last date of admission will be entertained for the purpose of claims/concessions etc. in connection with my admission.
7. I hereby agree to confirm to any rules, acts and laws enforced by Government and I hereby undertake that so long as I am a student of the College, I will do nothing either inside or outside the college, which may result in disciplinary action against me under the rules, acts and laws.
8. I fully understand that the Principal of the college where I would be admitted, will have right to expel / rusticate me from the college for any infringement of the Rules of conduct and prescribed by the college/university (if any).

**Date:**

**(Signature)**

**Place:**

**(Name of the Candidate)**

**APPENDIX 'A'**

**Sponsorship Letter for full-time candidates  
(This should be typed on letterhead of the Sponsoring Organization)**

**CERTIFICATE FROM THE HEAD OF THE ORGANISATION**

**Mr./Ms. \_\_\_\_\_**

**who is working in this Industry / Organization / Institute, is hereby sponsored for M.E. (Full time) course in \_\_\_\_\_ for the academic session 2010-11. In case of his/her selection for the course he/she will be relieved for the complete duration of course.**

Date : \_\_\_\_\_

Signature : \_\_\_\_\_

Name : \_\_\_\_\_

Designation : \_\_\_\_\_

Office Seal: \_\_\_\_\_

**PROFORMA: 1**

Specimen application form for cancellation of admission:  
(To be submitted in duplicate)

Date:

To,  
The Principal,  
K. K. Wagh Institute of Engineering Education & Research,  
Panchavati, Nashik.

Sir,

Full name of candidate : \_\_\_\_\_  
Name of Department (Admitted) : \_\_\_\_\_  
Programme: \_\_\_\_\_ Date of Admission: \_\_\_\_\_  
Category of seat sponsored / Non sponsored: \_\_\_\_\_  
Amount of fees paid (Rs.): \_\_\_\_\_  
Fee Receipt No. & Date: \_\_\_\_\_ (Attach Xerox copy)

**Undertaking:**

I am fully aware that after cancellation, I forfeit my claim on admission in any round by competent Authority. Please refund the amount due to me after deducting the cancellation charges by Cheque / DD, after deducting commission charges, if any.

I request to kindly return my original documents.

\_\_\_\_\_  
Signature of Candidate

Received following original documents from the admission authority.

- 1.
- 2.
- 3.
- 4.

**Signature of Candidate**



Karmaveer Kakasaheb Wagh Education Society's

### KARMAVEER KAKASAHEB WAGH INSTITUTE OF ENGINEERING EDUCATION & RESEARCH

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Mail: [kkwcoe\\_nsk@sancharnet.in](mailto:kkwcoe_nsk@sancharnet.in), [kkwe\\_office@dataone.in](mailto:kkwe_office@dataone.in) WEBSITE: [www.kkwagh.org](http://www.kkwagh.org)

#### APPLICATION FOR ADMISSION TO FIRST YEAR MASTER OF ENGINEERING COURSE OF UNIVERSITY OF PUNE FOR THE YEAR 2010-2011

Date of Reporting: \_\_\_\_\_

Institute PR No. 

--	--	--	--	--	--	--	--

Course for which admission is sought {√ Tick mark in appropriate box}:

ME Computer Engineering.

M.E. Electronics (VLSI & Embedded Systems).

Paste the Photograph inside the box only

Don't Staple or Pin the photograph

1. Name of the student: \_\_\_\_\_

2. Address

Correspondence Address	Permanent Address (Where parents Stay)
_____	_____
_____	_____
_____	_____
PIN _____ Ph.No. R. _____	PIN _____ Ph.No. R- _____
PP. _____	PP. _____
Mob. _____	Mob. _____
E-mail ID: _____	E-mail ID: _____

3. Date of Birth: \_\_\_\_\_ Age: \_\_\_\_\_ Birth Place & District: \_\_\_\_\_  
{√ Tick mark in appropriate box, as applicable}

a) Male  Female  b) GATE  Non Gate

c) Open  SC  ST  OBC  Others  (Write Category)

d) Are you a Sponsored Candidate Yes/No \_\_\_\_\_  
If "Yes" complete item 4, if no skip item 4.

4. Details of Post held \_\_\_\_\_

Organization \_\_\_\_\_

Work Experience: Industrial - Year \_\_\_\_\_ Month \_\_\_\_\_

Teaching - Year \_\_\_\_\_ Month \_\_\_\_\_

5. Passport No.: \_\_\_\_\_ Issue Date: \_\_\_\_\_ Expiry Date: \_\_\_\_\_

6. a) Details of Qualifying Examination:

Year of Passing	Degree Obtained	Branch	University	% of Marks Obtained	Remarks if any

b) Details of GATE Score on the basis of which admission is being sought.

Year \_\_\_\_\_ Branch \_\_\_\_\_ Percentile \_\_\_\_\_

(Student to keep three sets of attested Xerox with them for subsequent use)

**DECLARATION**

- 1) I hereby declare that I have carefully read and understood the instruction and particulars supplied to me by the Institute and all entries filled by me in this form are true to the best of my knowledge and belief.
- 2) I am aware of the law regarding prohibition of ragging as well as the punishments, and if I am found guilty of offence of ragging and / or abetting ragging, I am liable to be punished appropriately.
- 3) I Will strictly adhere to the rules, regulations and discipline of the Institute

Place: NASHIK

Date: \_\_\_\_\_

Signature of the student

**Attested copies of certificate Enclosed:**

{√ Tick mark in appropriate box, as applicable}

- |  |                          |                                |                          |
|--|--------------------------|--------------------------------|--------------------------|
| 1. Qualifying Exam Certificate         | <input type="checkbox"/> | 6. Caste Certificate           | <input type="checkbox"/> |
| 2. GATE Score Card                     | <input type="checkbox"/> | 7. Caste Validity Certificate  | <input type="checkbox"/> |
| 3. Mark List Of Qualifying Examination | <input type="checkbox"/> | 8. Migration Certificate       | <input type="checkbox"/> |
| 4. Copy of Transfer Certificate        | <input type="checkbox"/> | 9. Work Experience Certificate | <input type="checkbox"/> |
| 5. Sponsorship Certificate             | <input type="checkbox"/> |                                |                          |

Verified by

Scrutiny Officer

**Sign. of Admission I/c**

**Student Section**

**Admitted / Not Admitted**

**(Official Use)**

HOD's Remark: \_\_\_\_\_

**(Student has to fill this column)**

**Sign. of HOD**

**Provisionally admitted /Not admitted** \_\_\_\_\_

**Admission I/C / Student Sect.**

**PRINCIPAL**

Accounts Remarks –

Fees Paid Rs. \_\_\_\_\_

1000 X	
500 X	
100 X	
50 X	
20 X	
10 X	
5 X	
2 X	
1 X	
<b>Total Rs.</b>	

Fee Dues: \_\_\_\_\_ D.D. No. \_\_\_\_\_ Date: \_\_\_\_\_

Receipt No. \_\_\_\_\_ Date \_\_\_\_\_ Bank: \_\_\_\_\_ For Rs. \_\_\_\_\_

D.D. No. \_\_\_\_\_ Date: \_\_\_\_\_ Bank: \_\_\_\_\_ For Rs. \_\_\_\_\_

**Cashier's Signature**