

**K. K. Wagh Institute of Engineering Education and Research,
Nashik.**

Electronics & Telecommunication Department IETE Students Forum & SAEI Organizes

“EUREKA 2010”

State level Circuit Design & Mini Project Contest

Date: 13th March 2010.

Guide lines for Mini Project Contest

- A. Mini Project contest is open for TE students of Electronics / E & TC/
Instrumentation engineering.
- B. Maximum five participants are allowed in a group.
- C. Registration fees per participant is Rs. 100/-
- D. Synopsis to be submitted should include title of Project, block diagram & brief
working of project, name of institute and address.
- E. Synopsis should not exceed two pages (A4-size).
- F. Mail your synopsis to etceureka10@gmail.com before 27th February 2010.
- G. Acceptance will be intimated by email on 2nd March 2010.
- H. Last date for registration of selected entries in prescribed form by email is 8th
March 2010.

Guide lines for Circuit Design Contest

- A. On paper circuit Design contest is open for SE & TE students of Electronics / E
& TC/ Instrumentation engineering
- B. Maximum two participants are allowed in a group.
- C. Registration fees per group is Rs. 150/-
- D. Separate Problem statement will be given to each group.
- E. Total time allotted for paper circuit design is maximum 2 hrs.
- F. Required Manuals for component selection will be provided by organizers.
- G. Mail your entries in prescribed format to etceureka10@gmail.com before 27th
February 2010.
- H. For any clarification contact ;

Program coordinators: i) Prof. M. P. Satone

Mob. 9890026374.

ii) Prof. S. P. Ugale

Mob. 9881490970.

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Registration Form for Mini Project Contest

1. Name of institute and address: -
2. Title of Project:-
3. Hardware requirements of project:-
4. The participating students will have to make arrangements for PC if required and any specific hardware or software which cannot be satisfied by K.K.Wagh IEER, Nashik.
5. Details of students in mini-project group:-

Sr. No	Name of student	Branch	Telephone No.	E-mail.
1.				
2.				
3.				
4.				
5.				

Date: - _____

Signature
Principal/HOD with stamp

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Registration Form for Circuit Design contest

1. Name of institute and address: -
2. Details of students in Circuit Design group:-

Sr. No	Name of student	Branch	Telephone No.	E-mail.
1.				
2.				

Date: - _____

Signature
Principal/HOD with stamp